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** CONTINUING DATA ***** *NONE LV*** FOREIGN APPLICATIONS ***** *NONE LV*

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** 03/12/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>LV</i>	STATE OR COUNTRY AZ	SHEETS DRAWING 3	TOTAL CLAIMS 47	INDEPENDENT CLAIMS 3
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ADDRESS

022434

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TITLE

Method for fabrication of semiconductor interconnect structure with reduced capacitance, leakage current, and improved breakdown voltage

FILING FEE

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)